

ABOUT THE COLLEGE

PSG Institute of Technology and Applied Research, started in the year 2014, is an AICTE approved institution and affiliated to Anna University, Chennai. The latest initiative of PSG and Sons' Charities, aims to realize its objective of enhancing youth empowerment through technical education. This institute caters to various engineering disciplines, focusing on learning, industry engagement of students, innovative and inclusive pedagogy, and ethics. This institute aims at effective transfer of knowledge, pursuit of truth and moulding the students to become ideal citizens of the country. Within a short span of time, the institute has emerged to be one of the most preferred institutes for the engineering aspirants in TamilNadu.

ABOUT THE EEE DEPARTMENT

The Department of Electrical and Electronics Engineering accredited by NBA was established in the year of 2014. The mission of the department is to enable students become successful technocrats, and contribute to the progress of society. The department has faculty members with specializations in Power Systems, Electrical Machines, Power Electronics, Control Engineering, and Embedded Systems and some of them have a rich industry experience also. While the senior faculty members are PhD degree holders, others are actively pursuing research. The department has set up Centre of Excellence and signed MoU with industry leaders, thereby providing ample opportunities for faculty members and students to interact with industry experts.

ABOUT THE ECE DEPARTMENT

The Department of Electronics and Communication Engineering strives to produce top-notch engineers with global competence for industry, research and academia. The major objective of the department is to foster innovation, design, and creation of eco-sustainable products to meet the growing technological demands. The ECE department serves over 250 undergraduate students with a team of dedicated faculty members who are committed towards teaching and research in the areas of Signal Processing, Electronic Circuit design, Communication Systems, VLSI, Machine Learning, Internet of Things (IoT) and Embedded Systems. They motivate the students to do real - time projects and contribute to the society. The department has fully equipped laboratories with sophisticated equipment for conducting laboratory courses, research and consultancy activities.

ORGANIZING COMMITTEE

CHIEF PATRON **Shri. Gopalakrishnan L**
Managing Trustee
PSG Institutions
Coimbatore

PATRON **Dr. Chandramohan G**
Principal
PSG iTech, Coimbatore

COORDINATORS **Dr. Vasu C.L**
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IEEE MADRAS SECTION



Course Title
**ARTIFICIAL INTELLIGENCE FOR
SIGNAL PROCESSING APPLICATIONS**

Date
27.03.2023 - 31.03.2023

Co-ordinators
Dr. Vasu C.L
Dr. Vetrivelan P

Venue
**PSG INSTITUTE OF TECHNOLOGY AND
APPLIED RESEARCH, COIMBATORE**

Organized by
IEEE MADRAS SECTION
Chennai - 600 025

&
**DEPARTMENT OF EEE & ECE
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PSG INSTITUTE OF TECHNOLOGY

AND APPLIED RESEARCH



Neelambur , Coimbatore - 641 062

Department of Electrical and Electronics Engineering

&

Department of Electronics and Communication Engineering

IEEE Sponsored Five - Day FDP on

Artificial Intelligence for

Signal Processing Applications

Registration form

Name :

Qualification :

Designation :

Experience : Teaching: ...Yrs, Ind:.....Yrs

Organization :

Communication Address :

Mobile Number:

Email- id :

Details of registration fee :

Is accommodation required? Yes/No

Do you have IEEE membership? Yes/No

If yes, mention IEEE membership no:

Declaration:

I declare that the details furnished are true to my knowledge.

Signature of the applicant

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms..... is an employee of our Institute/Organization and is hereby sponsored.

Place:

Signature of the

Date :

Sponsoring Authority

ABOUT THE FDP

The purpose of the Faculty Development Programme (FDP) is to provide advanced knowledge in artificial intelligence for signal processing applications. Signal processing is a subfield of electrical, electronics and communications engineering that models and analyses data representation of physical events. It is central to the digital world. Signal processing is responsible for the operation of speech and audio, autonomous driving, image processing, wearable technology, and communication systems. This programme will be beneficial to research scholars for gaining knowledge and practical skills

OBJECTIVES

The primary goals of FDP are :

- Introduce participants to cutting-edge technology in the field of artificial intelligence.
- Describe the idea of using artificial intelligence techniques in signal processing applications.
- To gain practical skills in areas related to AI in DSP.

TOPICS TO BE COVERED

- AI for digital signal processing
- Emerging trends in signal processing and its applications
- Research opportunities in AI for signal processing applications
- Industrial applications for AI
- Practical aspects of AI in signal processing System
- Hands on session using MATLAB/KNIME

RESOURCE PERSONS:

The sessions will be handled by experts from academia and industry

- Dr.Sabarimalai Manikandan.M, IIT palakad
- Dr.Maheswari.R, Professor, VIT, Chennai
- Dr.Esakkirajan.S, Professor, PSG College of Technology, Coimbatore
- Dr. Jude Hemanth, Professor, Karunya University, Coimbatore
- Mr.V.S.Ramesh, Texas Instruements University Program
- Dr.Sree Niranjanaa Bose, Robert Bosch, Bangalore
- Mr.Murugan.G, Head of AI, Smiths Detection, Bangalore

ELIGIBILITY:

Faculty members from AICTE approved institutions and deemed universities

Personnel from R&D organization and industry

REGISTRATION FEE:

For non-IEEE members - Rs. 750/-

For IEEE members - Rs 500/-

NOTE:

Payment has to be made in the form of DD drawn in favour of "PSG CNCE" payable at Coimbatore.

FOR ONLINE PAYMENT:

Bank Account No : 1481267367

Name of Account Holder : PSG CNCE

IFSC code : CBIN0280913

VENUE:

Department of ECE , PSG Institute of Technology and Applied Research, Coimbatore - 641062

IMPORTANT DATES:

Last date for registration : 23.03.2023

Intimation of selection : 24.03.2023

Registration link: <https://forms.gle/aeVkJXUJTVVeM1z6>