

AUTHOR GUIDELINES

Prospective authors are encouraged to submit full paper(s) highlighting the technical contributions and key results for review. Only original which have not been published or submitted elsewhere will be considered. Submissions should not exceed SIX A4 size pages in length, should use font size not smaller than 10 points, and have margins on all four sides as per the IEEE format.

Prospective authors may kindly submit their papers, as per the guidelines provided to the mail id: nc2sd@psgitech.ac.in

PUBLICATIONS

Selected papers will be published in SCOPUS Indexed Journals with additional fee.

IMPORTANT DATES

Full paper submission : 30.03.2023
Acceptance Notification : 07.04.2023
Camera ready paper submission & Conference Registration : 11.04.2023

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124th RANK IN NIRF 2022

4th National Conference

on

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(NC²SD 2023)

13th APRIL 2023



ORGANIZED BY

DEPARTMENT OF
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ABOUT THE INSTITUTION

PSG Institute of Technology and Applied Research (PSGiTech), started in the year 2014 by PSG and Sons' Charities, is an AICTE approved Institution and affiliated to Anna University, Chennai. It is currently offering undergraduate programmes in Civil Engineering, Computer Science and Engineering, Electrical and Electronics Engineering, Electronics and Communication Engineering, Mechanical Engineering, Artificial Intelligence and Data Science, Computer Science and Business Systems and also postgraduate programmes in Structural Engineering and Engineering Design. This Institution is equipped with Centers of Excellence such as Innovation center, NI Graphical System Design Center, Keysight RF circuit & System Design Lab, Festo Lab, NEXMOO Mobile & IOT, CISCO Network Academy and eYantra.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering has been established in order to impart futuristic value based quality education in Electronics and Communication Engineering and to facilitate dissemination of the acquired knowledge by the students for the welfare of the society and to enable the students to excel in competitive examinations for higher education and employment. The department has been recognised by Anna University as a research centre.

ABOUT THE CONFERENCE

The objective of the conference is to provide a platform for the academicians, researchers and industrial personal to disseminate knowledge and results of original research, discussions of practical insights and the reporting of experience relevant to Electronics and Communication Engineering and their applications. This conference will explore the new horizon of innovations from distinguished researchers, scientists and eminent authors in academia and industry, working for the advancements in Engineering, Science and Technology from all over the country. The conference envisaged to provide an opportunity for young students and researchers to find link for future collaborations in their career path. This conference includes intellectual disclosures and keynote addresses by reputed academicians, industry and high quality presentation from the best researchers in India.

VISION

To produce globally competent and socially responsible electronics and communication engineers who will contribute towards the technological advancements in industry, research and academia.

MISSION

- To impart high quality technical education aimed at producing Electronics and Communication Engineering of high caliber.
- To facilitate students to assimilate the theoretical concepts by providing experiential learning.
- To promote innovative research in cutting edge technologies and indigenous product development.
- To collaborate and establish Industry-Institute-Interactions and equip the students to meet the techno - societal challenges.
- To inculcate qualities of teamwork, leadership and entrepreneurial skills in order to foster technocracy.
- To instill the spirit of value based education and life-long learning with a strong commitment to ethical practices and service to society

KEYNOTE SPEAKER



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CALL FOR PAPERS

High quality research contributions describing original and unpublished results of conceptual, constructive, empirical or theoretical work in any one/combined areas of the themes of the conference are cordially invited.

The submission of papers include, but not limited to:

- 5G and wireless broad band networks
- Visible light communication
- Optical communication
- RF/Microwave/Antennas
- Satellite Communication
- Software defined and cognitive Networks
- Adaptive/Array Signal processing
- Adaptive security Architecture
- Biomedical system Design
- Internet of Things(IoT)
- Wireless Networks
- Microprocessor and Micro controllers
- Nano Technology
- MEMES/NEMS
- Machine Learning & Deep Learning
- Embedded and Control systems
- VLSI Architecture for high speed and low power formal verification
- Design for Testability
- RF IC Design
- Reconfigurable Logic/ Computation
- CMOS Analog, Mixed Signal and Digital Circuits
- Image Processing
- Artificial intelligence and pattern recognition
- Robotics
- Fuzzy and Neutral Systems
- Device Modelling and Simulation

REGISTRATION FEES

- Faculty / Scholar /Student : Rs. 1000
- Industry : Rs. 1500
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