

Guest Lecture on “VLSI Design for Low power SoC design”

The ECE Association (ECA) and IETE Students’ Forum (ISF) organized a guest lecture on the topic “VLSI Design for Low power SoC design” for II and III year students on 15th June 2022. Mr. N Sundara Balaji, Director Engineering, Qualcomm India Pvt. Ltd, Chennai, was the resource person. The resource person joined virtually and around 50 students benefitted. The session started with a welcome address by convener Dr. P Vetrivelan, HoD-ECE. The lecture began with low-power SoC design and plans to design low-power circuits.

Clock distribution, different types of voltage scaling, Gate level optimization technique, multi Vdd technique, PowerGating and various low power techniques used in the industry were discussed. He concluded the talk with a brief explanation on the design elements required to design low-power SoCs with Retention Flip Flop and level shifters. The session ended with a 10-minute interactive question and answer session. Student coordinators of the event were Mr Pranuv.R, Vice-President (ECA) and Ms Rama Lakshmi.S, Joint Secretary (ECA).



Mr. N Sundara Balaji, Director Engineering, Qualcomm India Pvt. Ltd, Chennai, addressing the audience on 15th June 2022